

Notice of References Cited

Application/Control No.

09/982,459

Applicant(s)/Patent Under

Reexamination
SCHMITT ET AL.

Examiner

A. M. Thompson

Art Unit

2825

Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,205,571	03-2001	Camporese et al.	716/2
	B	US-6,305,001	10-2001	Graef	716/12
	C	US-6,311,313	10-2001	Camporese et al.	716/6
	D	US-5,387,885	02-1995	Chi	333/100
	E	US-5,239,215	08-1993	Yamaguchi	307/480
	F	US-5,656,963	08-1997	Masleid et al.	327/297
	G	US-6,088,254	07-2000	Kermani	365/63
	H	US-5,467,040	11-1995	Nelson et al.	327/276
	I	US-6,150,865	11-2000	Fluxman et al.	327/292
	J	US-6,025,740	02-2000	Fukuyama	326/93
	K	US-6,204,713	03-2001	Adams et al.	327/295
	L	US-5,923,188	07-1999	Kametani et al.	326/93
	M	US-5,994,924	11-1999	Lee et al.	326/93

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	J. Burkis, Clock Tree Synthesis for High Performance ASICs, IEEE, page 9-8.1 - 9-8.3, August 1991.
	V	J-S Yim et al., A Floorplan-based Planning Methodology for Power and Clock Distribution in ASICs, Proceedings ACM/IEEE Conference on Design Automation, pages 766-771, June 1999.
	W	P.J. Restle et al., A Clock Distribution Network for Microprocessors, 2000 Symposium on VLSI Circuits, pages 184-187, April 2000.
	X	B. Lampson et al., A Processor for a High-Performance Personal Computer, Seventh Annual Symposium on Computer Architecture, pages 146-160, May 1980.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited

Application/Control No.

09/982,459

Applicant(s)/Patent Under
Reexamination
SCHMITT ET AL.

Examiner

A. M. Thompson

Art Unit

2825

Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,378,080 ✓	04-2000	Anjo et al.	713/500
	B	US-6,053,950 ✓	04-2000	Shinagawa	716/12
	C	US-5,896,055 ✓	04-1999	Toyonaga et al.	327/295
	D	US-5,911,063 ✓	06-1999	Allen et al.	395/555
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	C-S Wu et al., An Automatic Cell Characterization Environment for Cell-Based Design Methodology, IEEE, pages 326-329, May 1993.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a))
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.